**ESET 219 Digital Electronics**

Laboratory Report

Lab #

<title of lab>

Author:

Due Date:

All of the information contained in this report is my own work that I completed as part of this lab assignment. I have not used results or content from any unauthorized sources or fellow students.

<Printed Name> Date:

**Introduction**

Briefly describe what the purpose of the lab was. Give a high level overview of the tasks that had to be completed. Introduction should be brief. Specific details are dedicated to other sections of the lab report.

**Background**

Describe the background theory and/or general operation of logic circuit created in lab. For example, if the lab is to create an adder, you would describe what an adder does, how it works, etc. Use figures where appropriate. All figures should contain a caption below the figure. All figures and captions are to be center aligned.

**Implementation**

Describe the lab tasks in detail. What was the problem presented in each task? What items were required to complete each task? What were the constraints or limitations presented in each task?

Describe the methods used to address each of the tasks in lab. Insert figures where necessary. If a table is needed, put a caption above the table at the left hand corner. Insert schematics.

**Results**

Describe the results of each method used to address each of the tasks in lab. Include figures if necessary. If a picture is needed (i.e. picture of FPGA board, DMM, etc.) please make sure the photo is not blurry and easy to understand. Discuss any factors that may have impacted results in a negative way. Insert simulation

**Conclusion**

Summarize the tasks, how you solved them, and results of the task. Discuss problems encountered in each task (if any) and how they were solved.